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#	Y	X	Document ID	Issue Date	Pages	Title	Current OR	Current Ref	Referral Local	Invention	Q	C	P	A	U	W
1			US 2007/0252791 A1	20070421	23	Antenna interface and	455/121	455/125.1	455/125.1	Forcosh, Stanley S.						
2			US 2009/050108 A1	20090210	194	Integrated VCO having an impedance tuning range over process and temperature variations	354/18			Duncan, Ralph et al.						
3			US 2004/017098 A1	20041104	170	Variable gain amplifier for low voltage applications	455/252.1	455/127.2		Dur, Hani et al.						
4			US 2004/0212417 A1	20041020	128	Large gain range, high linearity, low noise CMOS VCA	307/363			Benzadi, Ayta R.						
5			US 2004/019338 A1	20040930	175	Variable transconductance variable gain amplifier using a degenerated differential pair	330/254	330/283		Nguyen, Thanh Cat et al.						
6			US 2004/0174220 A1	20040909	23	Tunable power amplifier matching circuit	269/305			Forcosh, Stanley S. et al.						
7			US 2004/016056 A1	20040819	89	Applications for differential signal coupler	312/186	257/627.046		Ward, Christopher M. et al.						
8			US 2004/0177227 A1	20040729	9	Block for with extended tuning range	455/191.1	455/138		BK, Wolfgang Peter et al.						
9			US 2003/010161 A1	20030113	25	MEMS based, oscillator antenna, clock generation and oscillator circuits and IC-like apparatus for use thereof	332/1176			KCT, Gangadhar, Mahesh K. et al.						
10			US 2003/0194979 A1	20031016	85	System and method for on-chip PLL lock-instrument	455/197.1	455/137.046, 455/183.1, 455/195.1		Venkatesh, Peter et al.						
11			US 2003/0162521 A1	20030828	56	System and method for on-chip laser tuning	455/302	455/137.046, 455/183		Venkatesh, Peter et al.						